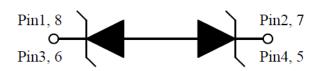
## Description

HWES2502PN is a low-capacitance Transient Voltage Suppressor (TVS) array designed to provide electrostatic discharge (ESD) protection for high-speed data interfaces.

With typical capacitance of 4.5pF only, HWES2502PN is designed to protect parasitic-sensitive systems against over-voltage and over-current transient events. It complies with IEC 61000-4-2 (ESD), Level 4 (±15kV air, ±8kV contact discharge), IEC 61000-4-4 (electrical fast transient - EFT) (40A, 5/50 ns), IEC 61000-4-5 (Surge) (20 A,8/20µs), very fast charged device model (CDM) ESD and cable discharge event (CDE), etc.

HWES2502PN is in a DFN-8L package. Each HWES2502PN device can protect two high-speed line pairs. The "flow-thru" design minimizes trace inductance and reduces voltage overshoot associated with ESD events. The combined features of low capacitance and high ESD robustness make HWES2502PN ideal for high-speed data port and high-frequency line (e.g., Gigabit Ethernet Ports) applications. The low clamping voltage of the HWES2502PN guarantees a minimum stress on the protected IC.

## Circuit Diagram



#### Features

Transient protection for high-speed data
lines IEC 61000-4-2 (ESD) ±30kV (Air)
±30kV (Contact)

IEC 61000-4-4 (EFT) 40A (5/50 ns) IEC 61000-4-5 (Surge) 20A (8/20μs)

- Package optimized for high-speed lines
- Provides protection for two line pairs
- Low capacitance: 4.5pF @ 2.5V (Typical)
- Low leakage current: 0.01uA @ V<sub>RWM</sub> (Typical)
- Low operating and clamping voltage
- Each I/O pin can withstand over 1000 ESD strikes for ±8kV contact discharge
- ROHS Compliant

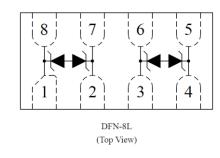
## Applications

- 10/100/1000M Ethernet Ports
- WAN/LAN Equipment
- Desktops, Servers and Notebooks
- Cellular Phones
- Switching Systems
- Audio/Video Inputs

## Mechanical Characteristics

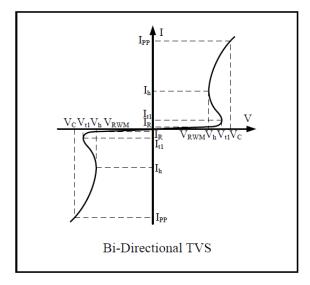
- DFN-8L package
- Flammability Rating: UL 94V-0
- Marking: Part number
- Packaging: Tape and Reel

## **■**Pin Configuration



## ■Electrical characteristics (Ta = 25 °C)

Symbol	Parameter					
$V_{\text{RWM}}$	Nominal Reverse Working Voltage					
$I_{R}$	Reverse Leakage Current @ V <sub>RWM</sub>					
$V_{\rm t1}$	Trigger Voltage					
l <sub>t1</sub>	Trigger Current @ V <sub>t1</sub>					
$V_{\rm h}$	Holding Voltage					
$I_{\rm h}$	Holding Current @ V <sub>h</sub>					
$V_{\rm C}$	Clamping Voltage @ I <sub>PP</sub>					
PP	Maximum Peak Pulse Current					
Cesd	Parasitic Capacitance					
$C_{\scriptscriptstyle{\Delta}}$	Variation in C <sub>ESD</sub> with Reverse Bias					



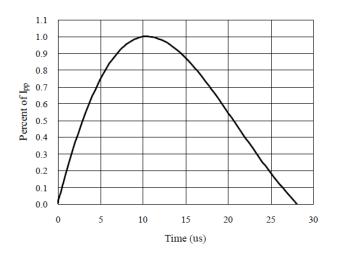
Symbol	Test Condition		Typical	Maximum	Units
VRWM				2.5	V
I <sub>R</sub>	$V_{RWM} = 2.5V, T = 25^{\circ}C$		0.01	0.05	uA
V <sub>t1</sub>	$I_{t1} = 1\mu A$	3	3.7	4.5	V
$V_h$	$I_h = 1mA$	3		4	V
V <sub>C</sub>	$I_{PP} = 2A$ , $t_p = 8/20 \mu s$ (Each Line)			5	V
V <sub>C</sub>	$I_{PP} = 10A$ , $t_p = 8/20\mu s$ (Each Line)			8	V
CESD	$V_R = 2.5V$ , $f = 1MHz$ (Each Line)		4.5	6	pF
C <sub>Δ</sub>	Pin1, 8 to 2, 7 & Pin3, 6 to Pin4, 5 $V_R = 0V \sim 2.5V$ , $f = 1MHz$		1.3		pF

## ■ Absolute Maximum Rating

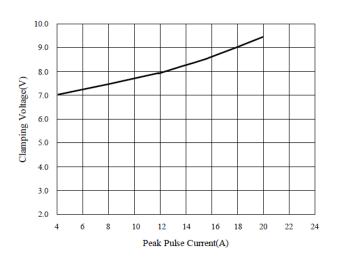
Symbol	Parameter	Value	Units
<b>I</b> PP	Peak Pulse Current (8/20μs)	20	Α
Ррк	Peak Pulse Power (8/20µs)	300	Watts
Vesd	ESD per IEC 61000-4-2 (Air)	±30	14) /
	ESD per IEC 61000-4-2 (Contact)	±30	kV
Торт	Operating Temperature	-45 to +85	°C
Тѕтс	Storage Temperature	-55 to +150	°C



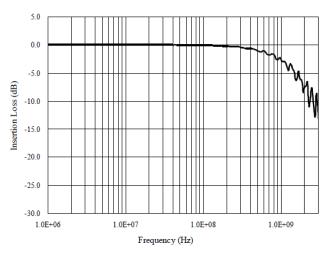
#### 8/20μs Pulse Waveform



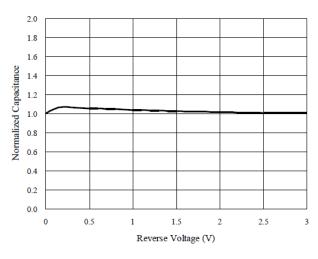
#### Clamping Voltage V<sub>C</sub> vs. Current I<sub>PP</sub>



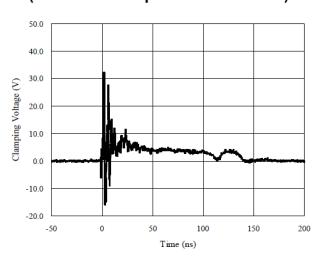
#### Insertion Loss S21



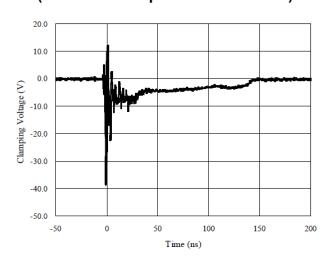
Normalized Capacitance vs. Voltage



### ESD Clamping of I/O to GND (+8kV Contact per IEC 61000-4-2)



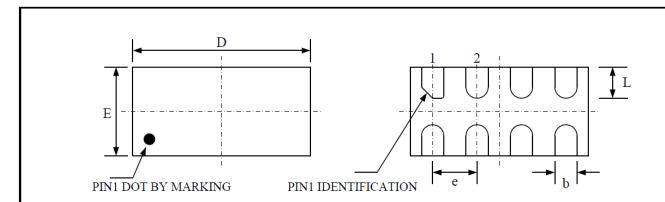
ESD Clamping of I/O to GND (-8kV Contact per IEC 61000-4-2)

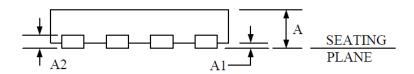


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# **Package Outline**

- DFN-8L Package
- Flow-Through
- MSL 1 & Thermally-Enhanced





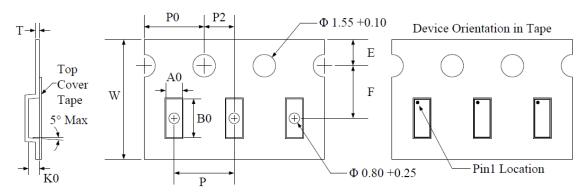
Package Dimensions (Controlling dimensions are in millimeters)

Symbol	Г	Dimensions (mm	1)	Dimensions (Inches)			
	Minimum	Typical	Maximum	Minimum	Typical	Maximum	
A	0.370	0.400	0.430	0.015	0.016	0.017	
A1	0.000	0.020	0.050	0.000	0.001	0.002	
A2	0.130				0.005		
ь	0.200	0.250	0.300	0.008	0.010	0.012	
D	1.900	2.000	2.100	0.075	0.079	0.083	
Е	0.900	1.000	1.100	0.035	0.039	0.043	
e		0.500 BSC			0.020 BSC		
L	0.300	0.350	0.400	0.012	0.014	0.016	
R	0.050	0.100	0.150	0.002	0.004	0.006	

Low Capacitance ESD/Surge Protection

#### Dec 2021 Ver. 1.1

# **Tape and Reel Specification**



Symbol	W	A0	В0	K0	Е	F	P	P0	P2	T
Dimensions (mm)	8.00+0.3 -0.1	1.15±0.05	2.15±0.05	0.48±0.05	1.75±0.1	3.5±0.10	4.0±0.1	4.0±0.1	2.0±0.05	0.2±0.03

